What is claimed is:

1. An apparatus comprising a plurality of low pass filters coupled to a common mode rejection amplifier to produce a band pass amplifier response.

2. The apparatus of claim 1 wherein the plurality of low pass filters includes a first low pass filter having a first corner frequency, and a second low pass filter having a second corner frequency, and wherein the band pass amplifier response is substantially between the first corner frequency and the second corner frequency.

3. The apparatus of claim 1 wherein at least one of the plurality of low pass filters comprises a programmable low pass filter.

4. The apparatus of claim 3 wherein the common mode rejection amplifier comprises a differential amplifier.

5. The apparatus of claim 4 wherein the differential amplifier includes two parallel-coupled differential input stages coupled to the plurality of low pass filters.

6. The apparatus of claim 1 further comprising an input stage having first and second differential outputs, wherein the plurality of low pass filters comprises first and second low pass filters coupled to the first differential output, and third and fourth low pass filters coupled to the second differential output.

7. The apparatus of claim 6 wherein:

the first and third low pass filters have substantially the same corner frequency; and

the second and fourth low pass filters have substantially the same corner frequency.

8. An apparatus comprising:

first and second differential input nodes;

first and second low pass filters coupled to the first differential input node;

third and fourth low pass filters coupled to the second differential input node; and

a differential amplifier with two parallel input stages coupled to the first, second, third, and fourth low pass filters.

9. The apparatus of claim 8 wherein at least one of the first, second, third, and fourth low pass filters has a programmable response.

10. The apparatus of claim 8 further comprising an automatic gain control circuit having a transistor to shunt a pair of differential output nodes from the differential amplifier.

11. The apparatus of claim 10 wherein the automatic gain control is coupled to sense a voltage on the first and second differential input nodes.

12. The apparatus of claim 10 wherein the automatic gain control is coupled to sense a voltage on the differential output nodes of the differential amplifier.

13. The apparatus of claim 8 further comprising an input amplifier coupled to receive an input signal and to drive the first and second differential input nodes.

14. The apparatus of claim 13 wherein the first and third low pass filters exhibit a corner frequency corresponding to a first corner frequency of a band pass response, and the second and fourth low pass filters exhibit a corner frequency corresponding to a second corner frequency of the band pass response.

15. An apparatus comprising:

a first amplifier;

a second amplifier having common mode rejection; and

a plurality of low pass filters coupled between the first and second amplifiers to set a band pass response by presenting unwanted frequency components in common mode to the second amplifier.

16. The apparatus of claim 15 wherein the plurality of low pass filters comprises:

first and second low pass filters coupled to a first differential output node of the first amplifier; and

third and fourth low pass filters coupled to a second differential output node of the first amplifier.

17. The apparatus of claim 16 wherein the second amplifier comprises:

a first differential input stage coupled to the first and third low pass filters; and

a second differential input stage in parallel with the first differential input stage, the second differential input stage coupled to the second and fourth low pass filters.

18. The apparatus of claim 17 wherein:

the first and third low pass filters exhibit a corner frequency corresponding to a first corner frequency of the band pass response; and

the second and fourth low pass filters exhibit a corner frequency corresponding to a second corner frequency of the band pass response.

19. The apparatus of claim 18 wherein the first, second, third, and fourth low pass filters are programmable.

20. An electronic system comprising:

an omni-directional antenna; and

a sliding band pass amplifier having an input coupled to the omni-directional antenna, the sliding band pass amplifier including a plurality of low pass filters and a differential amplifier coupled together to produce a band pass response having corner frequencies related to corner frequencies of the plurality of low pass filters.

21. The electronic system of claim 20 wherein at least one of the plurality of low pass filters is programmable.

22. The electronic system of claim 21 further comprising a processor to influence operation of the at least one programmable low pass filter.

23. The electronic system of claim 21 further comprising:

a mixer coupled to an output of the sliding band pass amplifier; and

a local oscillator to drive the mixer.

24. The electronic system of claim 23 further comprising a processor to influence operation of the local oscillator and the at least one programmable low pass filter.

25. The electronic system of claim 20 wherein the differential amplifier comprises two parallel differential input stages.

26. A method comprising:

setting a local oscillator; and

setting corner frequencies of low pass filters coupled to a differential amplifier to set a band pass response of the differential amplifier.

27. The method of claim 26 wherein setting corner frequencies sets frequencies to be rejected in a common mode of the differential amplifier.

28. The method of claim 26 wherein setting corner frequencies sets frequencies to be amplified in a differential mode of the differential amplifier.